

## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application: Divakaruni et al.

Today's Date: June 6, 2002

Serial No.: 09/718,850

Group Art Unit: 2811

Filed: 11/22/2000

Examiner: Gebremariam, Samuel A. T. Flowers

Title: LOGIC SOI STRUCTURE, PROCESS AND APPLICATION FOR VERTICAL BIPOLAR TRANSISTOR

## AMENDMENT UNDER 35 U.S.C. 132

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To the Assistant Commissioner of Patents  
Washington, D.C. 20231

JUN 6 2002

TECHNOLOGY CENTER 2800

Dear Sir:

In response to the Office Action of April 22, 2002, in which all of pending claims 1- 17 stand rejected, kindly amend the subject U.S. patent application as follows. The Patent Office is hereby authorized to charge Applicants' Deposit Account 09-0456 the sum of \$84.00, and such other fees as may be due for the entry and consideration of this amendment.

## CERTIFICATE OF MAILING

I hereby certify that, on the date shown below, this correspondence is being:

## MAIL

☐ deposited with the United States Postal Service with sufficient postage as first class mail in an envelope addressed to: Assistant Commissioner of Patents, Washington, DC 20231.

Date: 6/6/02

## FACSIMILE

☒ transmitted by facsimile to the Patent and Trademark Office.

Maryann Luisi  
Name  
Maryann Luisi  
Signature

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S/N 09/718,850